

## SUMMARY OF THE CLAIMS

1. (currently amended) An active matrix display comprising:
  - a substrate;
  - a device layer provided on ~~overlying~~ the substrate, the device layer comprising luminescent devices defining pixel units arrayed in a matrix, each luminescent device having an emitting area;
  - a circuitry layer provided between ~~overlying~~ the substrate and the device layer, the circuitry layer comprising pixel circuits for driving the respective luminescent devices, the pixel circuits defining the pixel units; and
  - contacts electrically connecting each of the luminescent devices with a corresponding pixel circuit, wherein the contacts are not provided under the emitting area of the luminescent devices, each positioned at the exterior of the emitting area of each pixel unit in the device layer and electrically connecting the corresponding luminescent device with the corresponding pixel circuit.
2. (original) An active matrix display according to Claim 1, wherein the contacts are arrayed in a single dimension for each row or column in the matrix.
3. (original) An active matrix display according to Claim 2, wherein the contacts for the pixel units belonging to two adjacent rows or columns in the matrix are arrayed in a single dimension between the two adjacent rows or columns.
4. (original) An active matrix display according to Claim 1, wherein the luminescent devices are organic electroluminescence devices, each comprising a first electrode, a second electrode, and an organic layer including an luminescent layer and lying between the first electrode and the second electrode.
5. (original) An active matrix display according to Claim 1, wherein the pixel circuits each comprise a thin-film transistor.
6. (currently amended) An active matrix display comprising:
  - a substrate;

a device layer provided on ~~overlying~~ the substrate, the device layer comprising luminescent devices defining pixel units, each luminescent device comprising a lower electrode, an upper electrode, and an organic layer ~~including a luminescent layer and lying~~ provided between the upper electrode and the lower electrode; and

a circuitry layer provided between ~~overlying~~ the substrate and the device layer, the circuitry layer comprising pixel circuits for driving the respective luminescent devices, the pixel circuits defining the pixel units;

wherein each lower electrode has a contact electrically connecting the corresponding luminescent device with the corresponding pixel circuit, and

wherein the upper electrode is not provided over ~~does not overlie~~ the contact.

7. (original) An active matrix display according to Claim 6, wherein the pixel circuits each comprise a thin-film transistor.